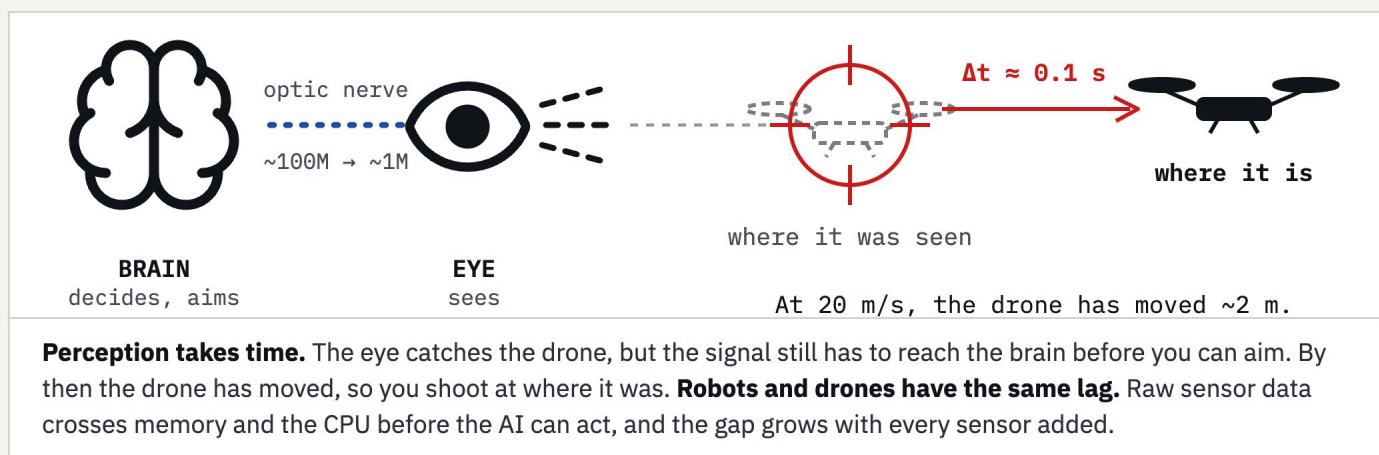
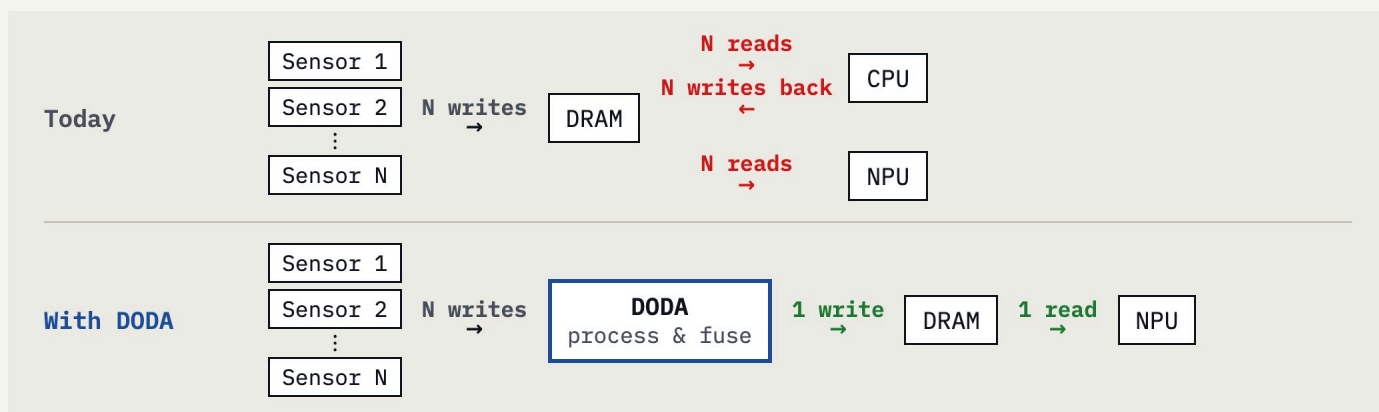




PROGRAMMABLE NEAR-SENSOR COMPUTING FOR PHYSICAL AI

Physical AI acts on where the world was, not where it is.

Sensor counts keep rising: 10–15 in a phone, 60–100 in a car, up to 25,000 in an aircraft. RiDM builds **DODA**, a runtime-programmable dataflow processor that sits beside the sensors and hands the AI fused, reduced data, so machines act on where the world is now.



Meet DODA

Dynamically Orchestrated Dataflow Architecture:

A processor built for heavily repetitive work such as sensor-data pre-processing.

10× faster than a CPU
1/100 of the power

Invented at NUS by the RiDM founders. Three PCT patent families filed by NUS, licensed to RiDM.

Who's building it

3

PhD researchers · Singapore

2

silicon engineers · Korea

Singapore leads core IP development in architecture

Korea leads the tape-out: IC design, SoC integration and MPW shuttles.

What's next

JUNE 2027 · FIRST TAPE-OUT

DODA silicon in 28nm.

H2 2027 · CLIENT TESTING

In potential clients' systems, against their current pipeline. Then first design wins.

Looking for

PoC PARTNERS

Teams that gain directly from DODA, as a chip or an FPGA overlay. High-precision robotics is a natural fit.

STRATEGIC INVESTORS

Capital plus access to robotics, sensing or semiconductor customers.

Invested by



TIPS secured. Korean government R&D grant for tech startups.

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